



Module designed to breakout signals to a solderless breadboard via Dupont cables.

Input/output device address selection requires one jumper shunt fitted to each set of jumper pins. Each bit of the required address can be set either high (1), low (0) or don't care (X) by fitting a shunt to one of these three positions. To select address range 50 hex to 53 hex, the required address bits are 0101 00XX. Address bit zero is always don't care (X). Usually the address decoding will require the CPU /M1 signal to be high to avoid interrupt acknowledge cycles selecting the input/output device. Thus JP8 will normally be linked high (1).

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| TITLE:<br>SC115: Breakout module for RC2014 |                             | REV: 1.0 |
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